

AMENDMENTS TO SPECIFICATION

Page 1, lines 17-24:

However, the prior art suffered from a disadvantage. For example, in the wireless receiver 1 only the first MCU 13 can receive and process signal s transmitted from wireless keyboard 17 and only the second MCU 15 can receive and process signals transmitted from wireless mouse 18 respectively. Hence, two MCUs are required in the wireless receiver 1 so as to receive both signals from wireless keyboard 17 and wireless mouse 18. This inevitably makes the wireless receiver 1 much too complicated, ~~resulting in making~~ a reduction of ~~in~~ size impossible. Thus a need for improvement exists.

Page 1, line 27 to Page 2, line 6:

It is therefore an object of the present invention to provide a wireless receiving apparatus comprising a single MCU (microprocessor control unit) which is capable of both receiving and processing signals having different frequencies transmitted from a plurality of peripheral devices simultaneously. By utilizing this apparatus, it is possible ~~of to~~ synchronously receiving ~~receive~~ signals so as to decrease the number of MCUs, simplify the circuitry, and reduce the size of the wireless receiving apparatus.

Page 4, lines 6-19:

~~In the~~ The invention signal receiving circuit 21 first sequentially receives various signals 261, 271, 281, 291, and 301 from a plurality of peripheral devices 26, 27, 28, 29, and 30. Then the received signals are sent to MCU 23 for identification processing. Next, a corresponding procedure of the signals stored in memory 25 is read out beforehand by the MCU so as to perform a predetermined processing procedure with respect to the signals. After at least one step of the predetermined processing procedure has been performed, ~~store the~~ index of the last finished step is stored in memory 25. Further, after a set of steps have been performed, MCU 23 can read the indices of the immediately previous set of steps from memory 25. Then, at least one step of the predetermined processing procedure is performed following the indices of previous

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steps by repeating the above process for sequentially processing the signals in a ~~mode of~~ multi-segment multi-task data processing mode until all signals have been processed. Finally, all processed signals are sent to computer 22.

Page 5, lines 2-4:

In step 303, after at least one step of the predetermined processing procedure has been performed with respect to the first signal, ~~stores~~ storing an index of the last finished step in memory 25.

Page 5, lines 9-11:

In step 306, after at least one step of the predetermined processing procedure has been performed with respect to the second signal, ~~stores~~ storing an index of the last finished step in memory 25.

Page 5, lines 12-18:

In step 307, after a set of steps (i.e., steps 301 to 306) have been performed, the MCU continues to perform unfinished steps with respect to the signals based on stored indices in memory for performing a next set of steps immediately ~~later~~ thereafter. ~~Above~~ The above process is ~~repeated~~ repeatedly performed. Further, a determination is made whether all processes have been performed ~~about~~ with respect to the signals based on the predetermined processing procedure with respect to the signals. If yes, the process goes to step 308. Otherwise, the process loops back to step 301.

Page 5, lines 21-23:

With this, it is possible ~~of to~~ synchronously processing ~~process~~ signals transmitted from the plurality of peripheral devices so as to decrease the number of required MCUs.

Page 6, lines 4-8:

Referring to FIG. 4, there is shown a flow chart illustrating a process according to a first embodiment of the invention. After MCU 23 has finished an identification, a process is performed with respect to the signal having the first frequency transmitted from signal receiving circuit 21 in a mode of multi-segment multi-task data processing mode. The steps of the process are as follows:

Page 8, lines 21-26:

In brief, the wireless receiving apparatus of the present invention comprises a single MCU which is capable of both receiving and processing signals having different frequencies transmitted from a plurality of peripheral devices simultaneously. By utilizing this apparatus, it is possible ~~of decreasing~~ to decrease the number of MCUs, simplifying the circuitry[[,]] and reducing the size of the wireless receiving apparatus.

Page 8, lines 27 to Page 9, line 8:

Referring to FIG. 2 again, there is shown another embodiment of the invention. The signal receiving circuit 21 receives ~~signal~~ signals from a plurality of peripheral devices by polling. That is, a signal including an identification query is sequentially transmit ~~signal about query of identification transmitted~~ to each peripheral device. In response, each peripheral device sends back an identification to signal receiving circuit 21 for identification. This can effect a successful connection between signal receiving circuit 21 and each peripheral device. Hence, signal receiving circuit 21 can sequentially receive ~~signal~~ signals transmitted from each peripheral device prior to sending to MCU 23 for further processing.